



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/054,653 Confirmation No.: 9448
First Named Inventor: Bulucea, Constantin Filing Date: 18 January 2002
Group Art Unit: 2814 Examiner: Farahani, D.
Atty. Docket No.: NS-5127 US
Title: Gate-Enhanced Junction Varactor With Gradual
Capacitance Variation
Assignee(s): National Semiconductor Corporation

Mountain View, California
28 October 2003

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**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97(b)**

Sir:

Pursuant to 37 CFR 1.56, 1.97, and 1.98, each document listed on the accompanying substitute PTO Form 1449 is called to the attention of the Examiner for the above patent application. A copy of each listed document is enclosed. Further enclosed is an English abstract of Japanese Patent Publication 7-226643.

One of the two cited documents is U.S. patent application 09/903,059 discussed in the specification of the present application and mentioned in the Information Disclosure Statement submitted 23 January 2003. To the extent that application 09/903,059 may not have been previously called to the Examiner's attention in connection with the present application in such a manner as to obligate the Examiner to consider U.S. application in examining the present application, application 09/903,059 is here officially called to the Examiner's attention in connection with the present application.

U.S. application 09/903,059, now pending, was published after the filing date of the present application, has the same inventorship as the present application, and is assigned to

Ronald J. Meetin
Attorney at Law
210 Central Avenue
Mountain View, CA
94043-4869
Tel.: 650-964-9767
Fax: 650-964-9779

the same assignee as the present application. As far as Applicant's Attorney can determine, application 09/903,059 is not prior art to the present application.

Japanese Patent Publication 7-226643 was cited during the prosecution of application 09/903,059.

Citation of each listed document shall not be construed as:

1. an admission that the document is necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described herein;
or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 CFR 1.56(b).

This information disclosure statement is submitted under the provisions of 37 CFR 1.97(b).

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Respectfully submitted,

Ronald J. Meetin

Ronald J. Meetin
Attorney for Applicant(s)
Reg. No. 29,089

210 Central Avenue
Mountain View, CA 94043-4869

Ronald J. Meetin
Attorney at Law
210 Central Avenue
Mountain View, CA
94043-4869

Tel.: 650-964-9767
Fax: 650-964-9779

U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.		Application No.		
				NS-5127 US		10/054,653		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				First Named Inventor:		Confirmation No.		
Substitute Form PTO 1449				Bulucea, Constantin		9448		
				Filing Date		Group		
				18 January 2002		2814		
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	09/903,059		Bulucea			10 July 2001	
	AB							
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL	7-226643	10/1995	Japan				X
	AM							
	AN							
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AQ							
	AR							
	AS							
Examiner			Date Considered					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw a line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								

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